Hogan & Hartson 81754.0118 Terunao HANAOKA Semiconductor Device and Method... EV 324 112 455 US 2 Drawing Sheets; Sheet 1 of 2

FIG.1

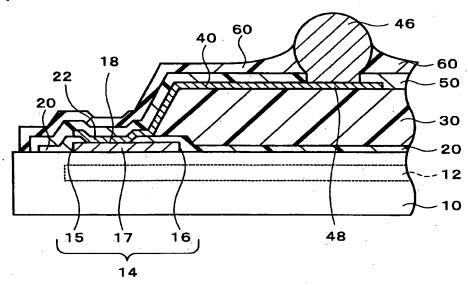
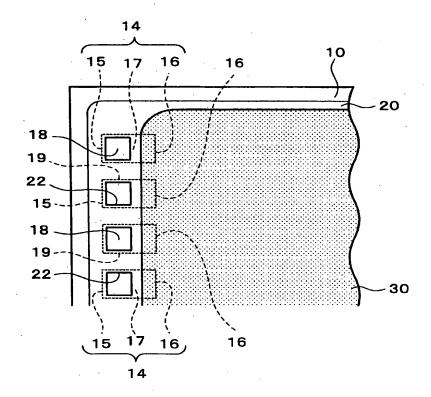


FIG.2



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FIG.3

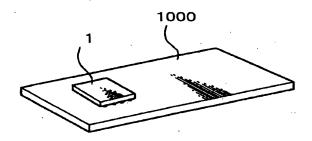


FIG.4

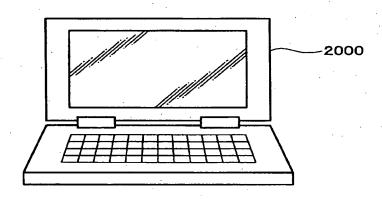


FIG.5

